



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

SEP 29 2003

JC35

Application of: Zhaohui Shen et al.

Serial No.: 09/684,868

Title: DIAGNOSTIC ARCHITECTURE USING FPGA CORE IN SYSTEM ON A CHIP DESIGN

Filed: October 6, 2000

Attorney Docket No.: 00-255 / 1496.00039

Examiner: Dinh, P.

Art Unit: 2825

In Response To: Office Action mailed June 27, 2003

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 26, 2003.

By: Mary Donna Berkley
Mary Donna Berkley

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AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed June 27, 2003

please amend the above-identified application as follows:

10/02/2003 SFELEKE1 00000137 122252 09684868

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